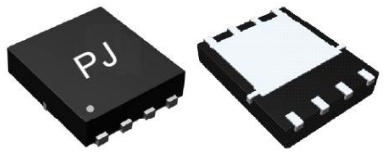
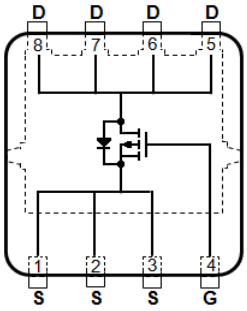
PJQ5413

30V P-Channel Enhancement Mode MOSFET

Voltage	-30 V	Current	-25 A

DFN5060-8L





Features

- $R_{DS(ON)}$, V_{GS}@-10V, I_D@-4A<30mΩ
- $R_{DS(ON)}$, V_{GS}@-4.5V, I_D@-2A<45mΩ
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : DFN5060-8L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0028 ounces, 0.08 grams

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current (Note 4)	T_C=25°C	I_D	-25	A
	T_C=100°C		-16	A
Pulsed Drain Current (Note 1)	T_C=25°C	I_{DM}	-50	A
Power Dissipation	T_C=25°C	P_D	30	W
	T_C=100°C		12	W
Continuous Drain Current (Note 4)	T_A=25°C	I_D	-6.5	A
	T_A=70°C		-5	A
Power Dissipation	T_A=25°C	P_D	2	W
	T_A=70°C		1.3	W
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal Resistance (Note 4,5)	Junction to Case	$R_{\theta JC}$	4.2	°C/W
	Junction to Ambient	$R_{\theta JA}$	62.5	°C/W

- Limited only By Maximum Junction Temperature

PJQ5413

Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=-250uA	-30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=-250uA	-1	-1.6	-2.5	
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=-10V, I_D=-4A	-	26	30	mΩ
		V_{GS}=-4.5V, I_D=-2A	-	36	45	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-30V, V_{GS}=0V	-	-	-1	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
Dynamic (Note 6)						
Total Gate Charge	Q_g	V_{DS}=-15V, I_D=-5A, V_{GS}=-4.5V (Note 1,2)	-	7.8	-	nC
Gate-Source Charge	Q_{gs}		-	2.7	-	
Gate-Drain Charge	Q_{gd}		-	2.8	-	
Input Capacitance	Ciss	V_{DS}=-15V, V_{GS}=0V, f=1MHZ	-	870	-	pF
Output Capacitance	Coss		-	130	-	
Reverse Transfer Capacitance	Crss		-	93	-	
Turn-On Delay Time	$td_{(on)}$	V_{DS}=-15V, I_D=-1A, V_{GS}=-10V, R_G=6Ω (Note 1,2)	-	6.5	-	ns
Turn-On Rise Time	t_r		-	8.8	-	
Turn-Off Delay Time	$td_{(off)}$		-	73	-	
Turn-Off Fall Time	t_f		-	44	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	-25	A
Diode Forward Voltage	V_{SD}	I_S=-1A, V_{GS}=0V	-	-0.75	-1	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J=25°C.
4. The maximum current rating is package limited.
5. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 $inch^2$ with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing.

PJQ5413

TYPICAL CHARACTERISTIC CURVES

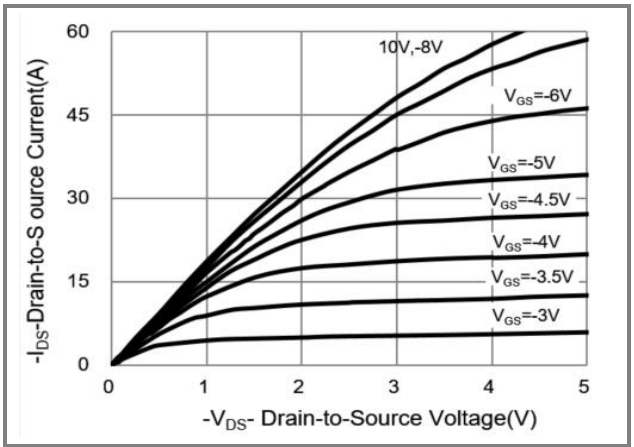


Fig.1 On-Region Characteristics

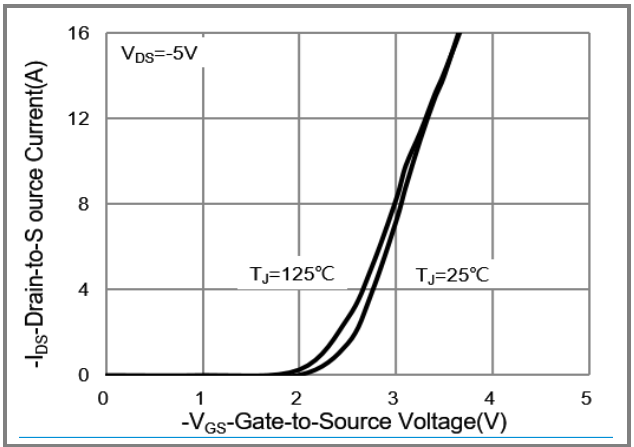


Fig.2 Transfer Characteristics

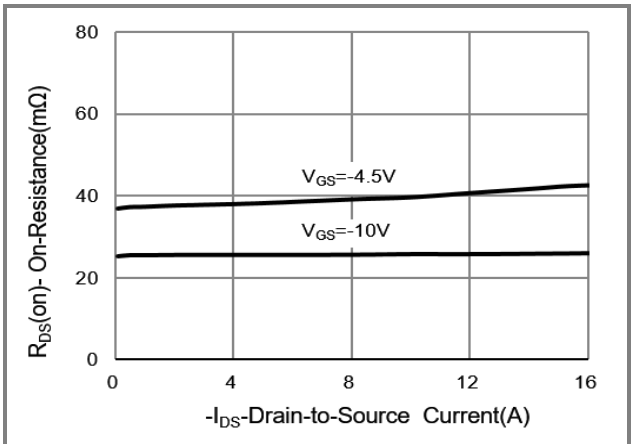


Fig.3 On-Resistance vs. Drain Current

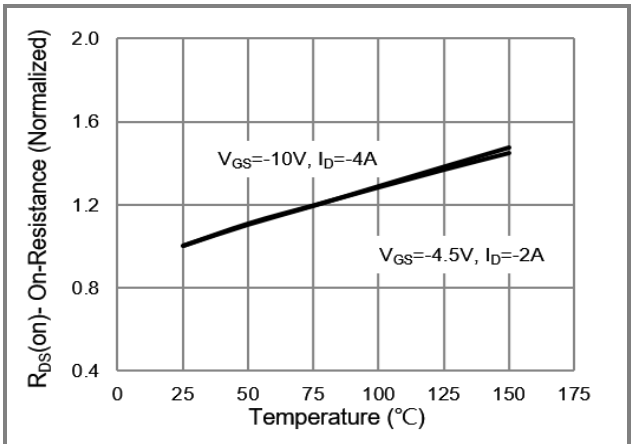


Fig.4 On-Resistance vs. Junction temperature

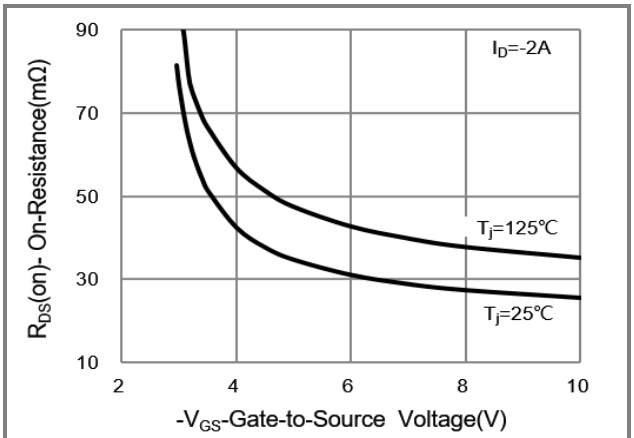


Fig.5 On-Resistance Variation with V_{GS}

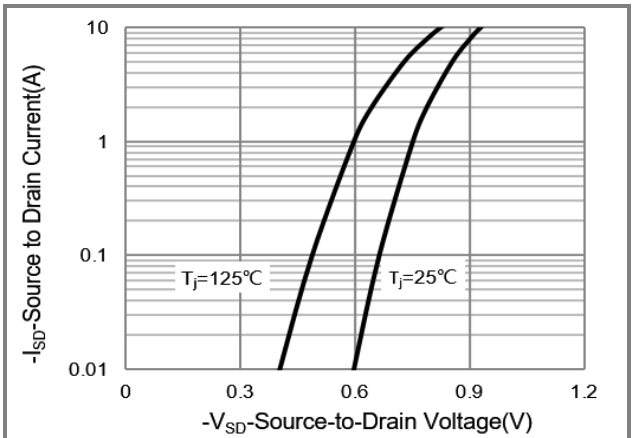


Fig.6 Body Diode Characteristics

PJQ5413

TYPICAL CHARACTERISTIC CURVES

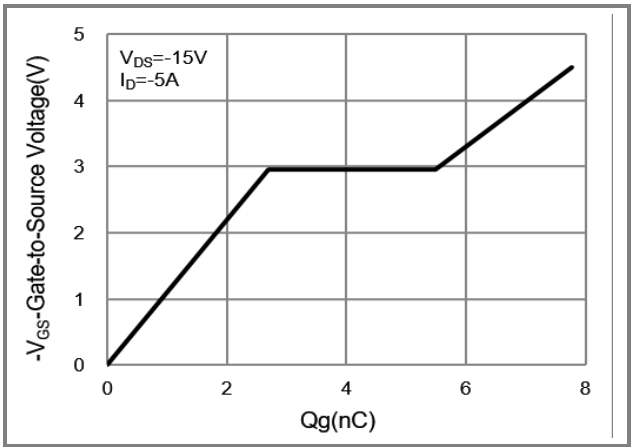


Fig.7 Gate-Charge Characteristics

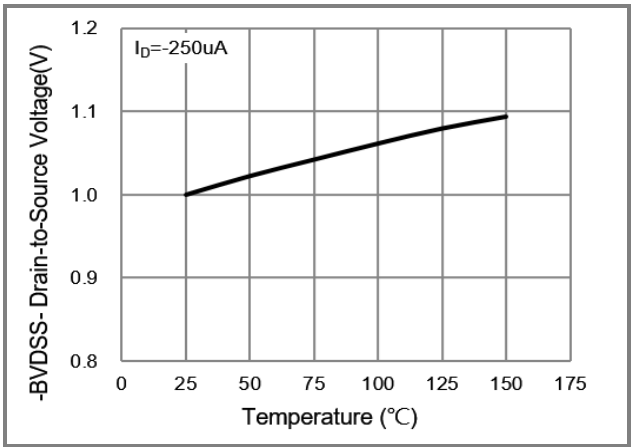


Fig.8 Breakdown Voltage Variation vs. Temperature

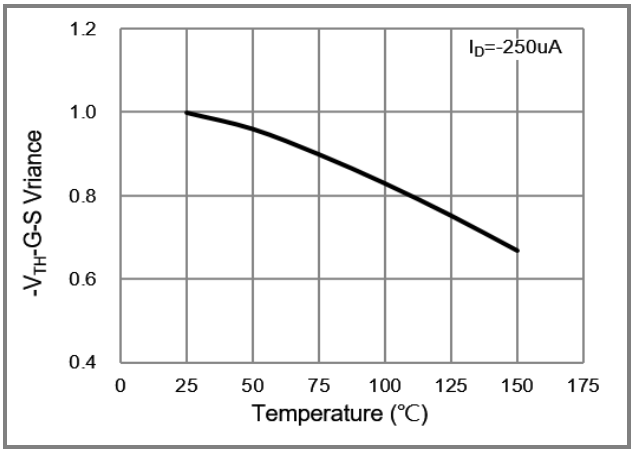


Fig.9 Threshold Voltage Variation with Temperature

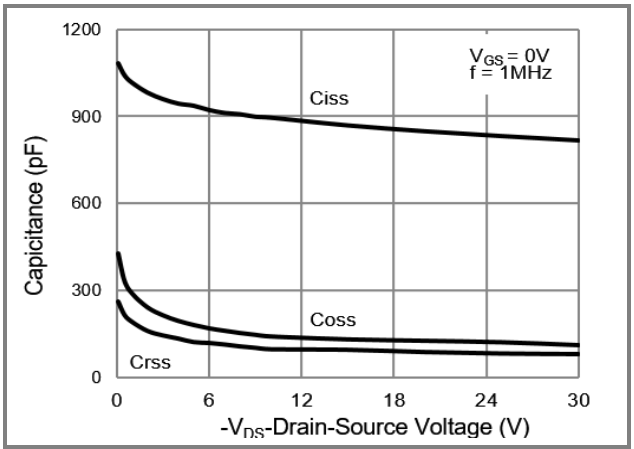


Fig.10 Capacitance vs. Drain-Source Voltage

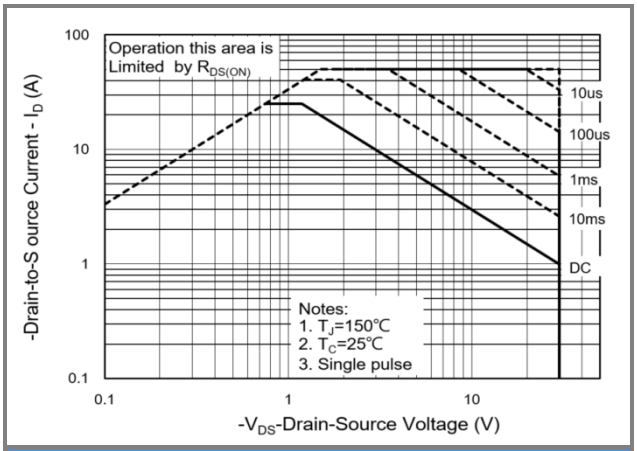


Fig.11 Maximum Safe Operating Area

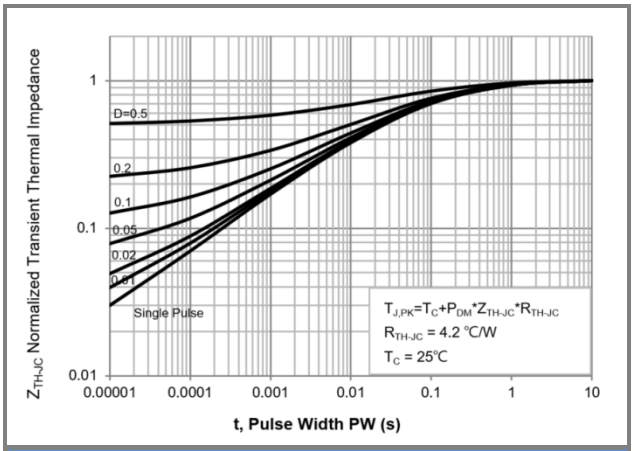


Fig.12 Normalized Thermal Transient Impedance

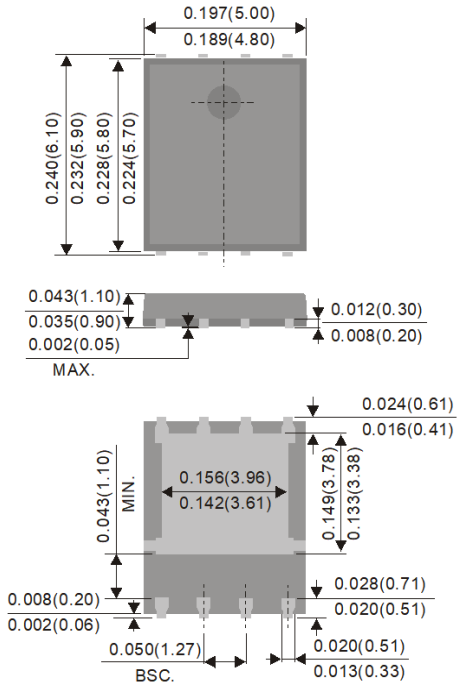
PJQ5413

Part No Packing Code Version

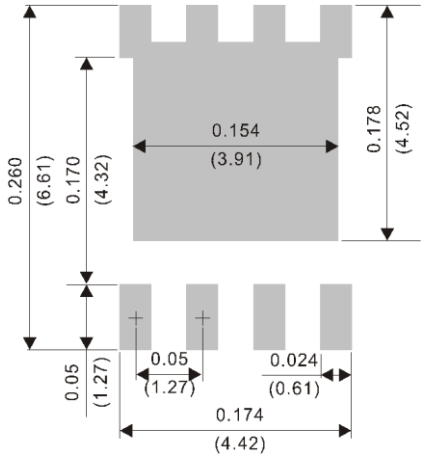
Part No Packing Code	Package Type	Packing Type	Marking	Version
PJQ5413_R2_00001	DFN5060-8L	3000pcs / 13” reel	Q5413	Halogen free

Packaging Information & Mounting Pad Layout

DFN5060-8L Dimension — Unit: inch(mm)



DFN5060-8L Pad Layout — Unit: inch(mm)



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