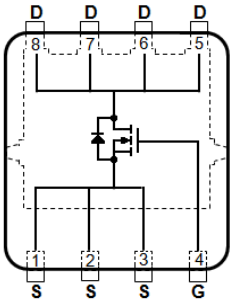
# PJQ4546P-AU

## 40V N-Channel Enhancement Mode MOSFET

| Voltage | 40 V | Current | 64 A |
|---|---|---|---|

**DFN3333-8L**






### Features

- $R_{DS(ON)}$, $V_{GS}$@10V, $I_D$@15A<5.6mΩ
- $R_{DS(ON)}$, $V_{GS}$@4.5V, $I_D$@10A<7.9mΩ
- Excellent FOM
- Logic Level Drive
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : DFN3333-8L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.03 grams

### Maximum Ratings and Thermal Characteristics ($T_A$=25°C unless otherwise noted)

| PARAMETER | | SYMBOL | LIMIT | UNITS |
|---|---|---|---|---|
| Drain-Source Voltage | | $V_{DS}$ | 40 | V |
| Gate-Source Voltage | | $V_{GS}$ | ±20 | |
| Continuous Drain Current(Note 3) | $T_C$=25°C | $I_D$ | 64 | A |
| | $T_C$=100°C | | 45 | |
| Pulsed Drain Current(Note 1) | $T_C$=25°C | $I_{DM}$ | 256 | |
| Power Dissipation | $T_C$=25°C | $P_D$ | 42 | W |
| | $T_C$=100°C | | 21 | |
| Continuous Drain Current(Note 4) | $T_A$=25°C | $I_D$ | 15.7 | A |
| | $T_A$=70°C | | 13.2 | |
| Power Dissipation | $T_A$=25°C | $P_D$ | 2.5 | W |
| | $T_A$=70°C | | 1.8 | |
| Single Pulse Avalanche Energy(Note 5) | | $E_{AS}$ | 81 | mJ |
| Operating Junction and Storage Temperature Range | | $T_J$,$T_{STG}$ | -55~175 | °C |
| Thermal Resistance(Note 4) | Junction to Case | $R_{\theta JC}$ | 3.6 | °C/W |
| | Junction to Ambient | $R_{\theta JA}$ | 60 | |

# PJQ4546P-AU

## Electrical Characteristics ($T_A$=25°C unless otherwise noted)

| PARAMETER | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNITS |
|---|---|---|---|---|---|---|
| **Static** | | | | | | |
| Drain-Source Breakdown Voltage | $BV_{DSS}$ | $V_{GS}$=0V, $I_D$=250uA | 40 | - | - | V |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}$=$V_{GS}$, $I_D$=50uA | 1.1 | 1.6 | 2.3 | |
| Drain-Source On-State Resistance | $R_{DS(on)}$ | $V_{GS}$=10V, $I_D$=15A | - | 4.5 | 5.6 | mΩ |
| | | $V_{GS}$=4.5V, $I_D$=10A | - | 6.1 | 7.9 | |
| Zero Gate Voltage Drain Current | $I_{DSS}$ | $V_{DS}$=40V, $V_{GS}$=0V | - | - | 1 | uA |
| Gate-Source Leakage Current | $I_{GSS}$ | $V_{GS}$=±20V, $V_{DS}$=0V | - | - | ±100 | nA |
| **Dynamic**(Note 6) | | | | | | |
| Total Gate Charge | $Q_g$ | $V_{DS}$=32V, $I_D$=15A, $V_{GS}$=10V(Note 2,3) | - | 20 | - | nC |
| Gate-Source Charge | $Q_{gs}$ | | - | 3.1 | - | |
| Gate-Drain Charge | $Q_{gd}$ | | - | 6.4 | - | |
| Input Capacitance | Ciss | $V_{DS}$=25V, $V_{GS}$=0V, f=1MHZ | - | 1320 | - | pF |
| Output Capacitance | Coss | | - | 250 | - | |
| Reverse Transfer Capacitance | Crss | | - | 30 | - | |
| Gate resistance | $R_g$ | f=1MHZ | - | 0.8 | - | Ω |
| Turn-On Delay Time | $td_{(on)}$ | $V_{DS}$=32V, $I_D$=15A, $V_{GS}$=10V, $R_G$=3Ω (Note 2,3) | - | 11 | - | ns |
| Turn-On Rise Time | $t_r$ | | - | 3 | - | |
| Turn-Off Delay Time | $td_{(off)}$ | | - | 28 | - | |
| Turn-Off Fall Time | $t_f$ | | - | 5 | - | |
| **Drain-Source Diode** | | | | | | |
| Diode Forward Current | $I_S$ | $T_C$=25°C | - | - | 64 | A |
| Pulsed Diode Forward Current | $I_{SM}$ | | - | - | 256 | |
| Diode Forward Voltage | $V_{SD}$ | $I_S$=20A, $V_{GS}$=0V | - | 0.85 | 1.3 | V |
| Reverse Recovery Time | Trr | $V_{GS}$=0V, $I_S$=20A dI$_S$/dt=100A/us(Note 2,3) | - | 23 | - | ns |
| Reverse Recovery Charge | Qrr | | - | 15 | - | nC |

NOTES :

1. Pulse width≤100us, Duty cycle≤2%.
2. Essentially independent of operating temperature typical characteristics.
3. Chip capability with an $R_{\theta JC}$=3.6°C/W.
4. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch$^2$ with 2oz.square pad of copper.
5. The test condition is L=0.5mH, $I_{AS}$=18A, $V_{DD}$=30V, $V_{GS}$=10V, Starting TJ=25°C.
6. Guaranteed by design, not subject to production testing.

# PJQ4546P-AU

## TYPICAL CHARACTERISTIC CURVES

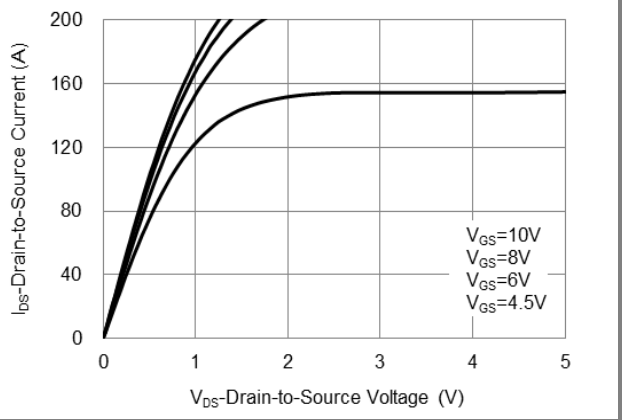



Fig.1 On-Region Characteristics

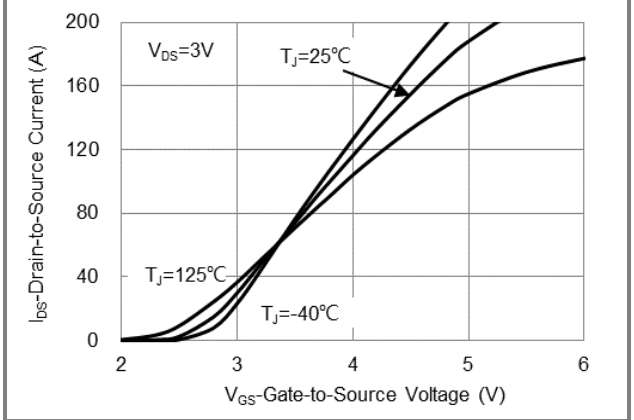



Fig.2 Transfer Characteristics

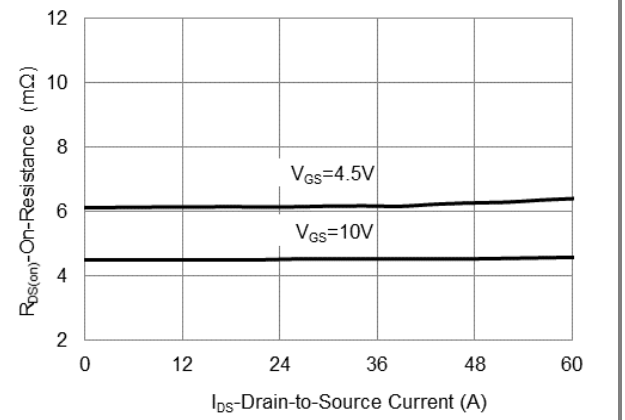



Fig.3 On-Resistance vs. Drain Current

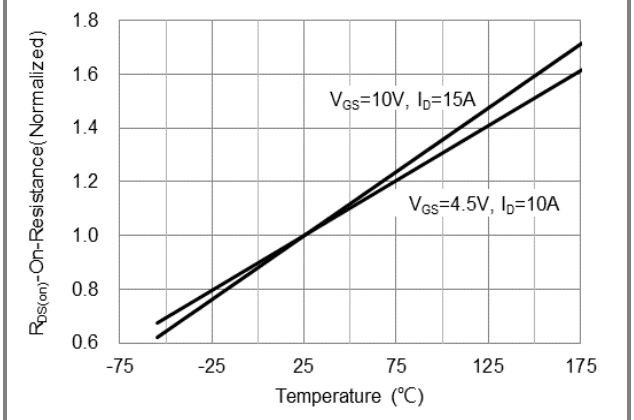



Fig.4 On-Resistance vs. Junction temperature

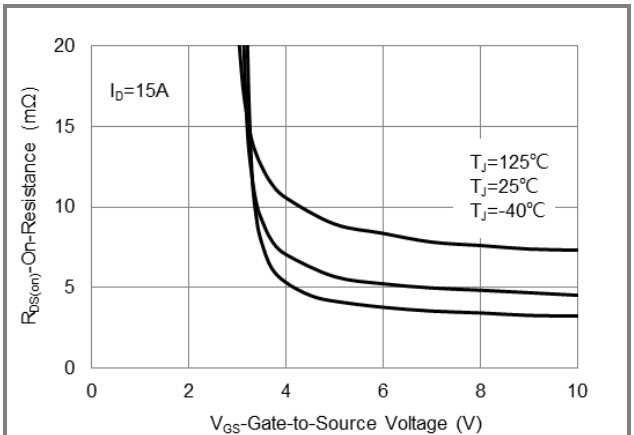



Fig.5 On-Resistance Variation with $V_{GS}$

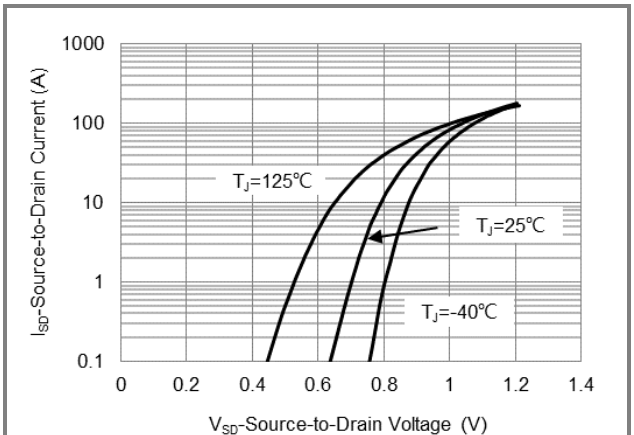



Fig.6 Source-Drain Diode Forward Voltage

# PJQ4546P-AU

## TYPICAL CHARACTERISTIC CURVES

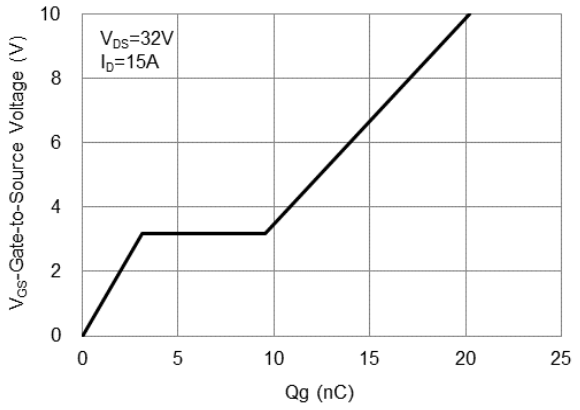



Fig.7 Gate-Charge Characteristics

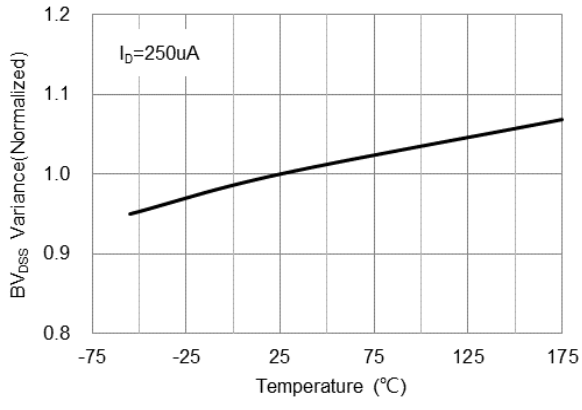



Fig.8 Breakdown Voltage Variation vs. Temperature

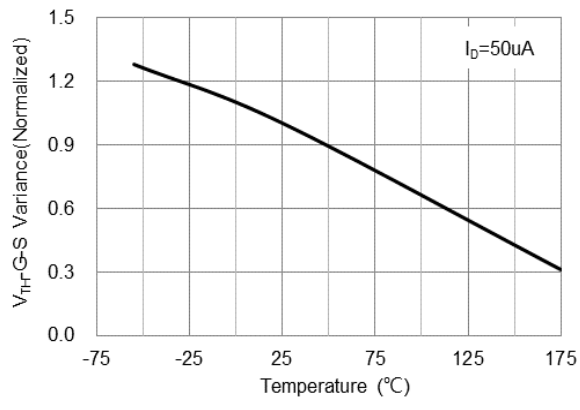



Fig.9 Threshold Voltage Variation with Temperature

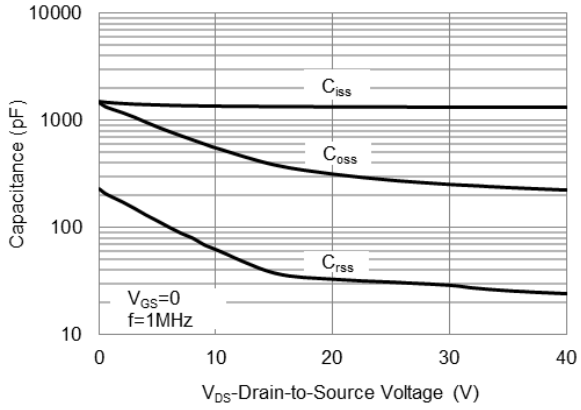



Fig.10 Capacitance vs. Drain-Source Voltage

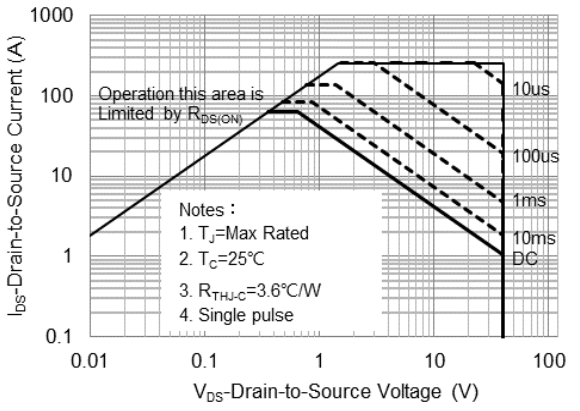



Fig.11 Maximum Safe Operating Area

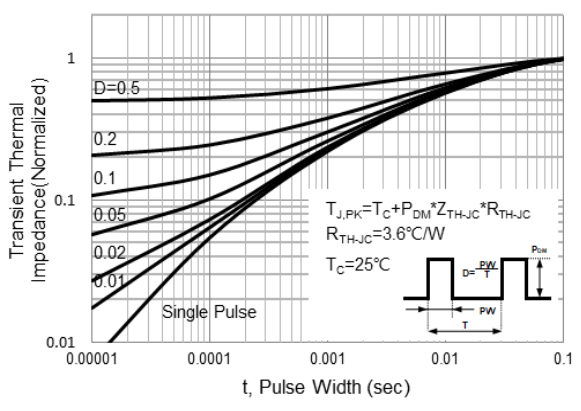



Fig.12 Normalized Transient Thermal Impedance

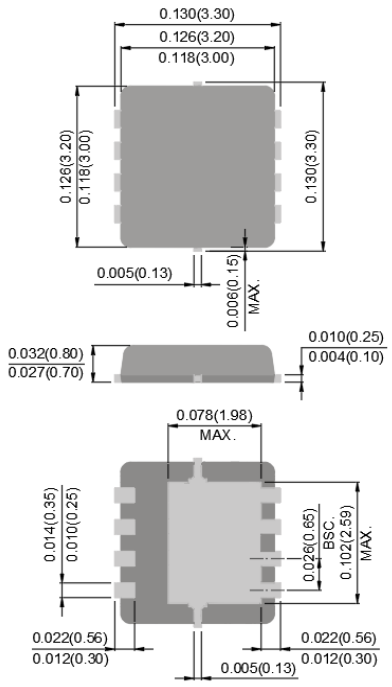
# PJQ4546P-AU

## Product and Packing Information

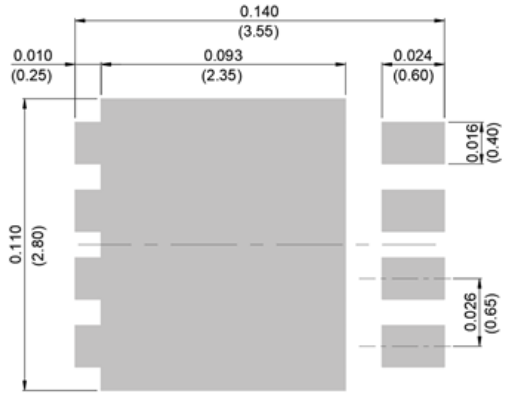
| Part No. | Package Type | Packing Type | Marking |
|---|---|---|---|
| PJQ4546P-AU | DFN3333-8L | 5K pcs / 13” reel | 4546 |

## Packaging Information & Mounting Pad Layout

DFN3333-8L Dimension — Unit: inch(mm)




DFN3333-8L Pad Layout — Unit: inch(mm)



# PJQ4546P-AU

## Disclaimer

- Reproducing and modifying information of the document is prohibited without permission from Panjit International Inc..
- Panjit International Inc. reserves the rights to make changes of the content herein the document anytime without notification. Please refer to our website for the latest document.
- Panjit International Inc. disclaims any and all liability arising out of the application or use of any product including damages incidentally and consequentially occurred.
- Panjit International Inc. does not assume any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.
- Applications shown on the herein document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications. Panjit International Inc. makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.
- The products shown herein are not designed and authorized for equipments relating to human life and for any applications concerning life-saving or life-sustaining, such as medical instruments, aerospace machinery et cetera. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Panjit International Inc. for any damages resulting from such improper use or sale.
- Since Panjit uses lot number as the tracking base, please provide the lot number for tracking when complaining.

ENTEGRECİ

Bu belge www.entegreci.com üzerinden sunulmaktadır.

# Elektronik komponent tedarikinde Entegreci

Entegreci, Eskişehir merkezli, Türkiye geneline satış yapan bir elektronik komponent mağazasıdır. Entegreler (IC), mikrodenetleyiciler, transistörler, diyotlar, pasif elemanlar, konnektörler, modüller ve geliştirme kartları tek adetten seri üretim adetlerine kadar satılır. Ürün sayfalarında güncel stok durumunu, fiyatı ve datasheet bağlantısını bulabilirsiniz.

### Parça koduyla arama

Üretici parça numarasını yazın; stok ve fiyatı hemen görün.

**www.entegreci.com**

### Ürün kategorileri

Entegreden kapasitöre tüm elektronik komponent kategorileri.

**Kategorilere göz atın**

### Üreticiler

Aradığınız üreticinin ürünlerini tek sayfada inceleyin.

**Üreticileri görün**

### Toplu alım ve teklif

Listede olmayan ya da yüksek adetli parçalar için teklif isteyin.

**Teklif isteyin**

**www.entegreci.com** | **destek@entegreci.com** | **0850 305 90 34**

Entegreci is an online electronic components store based in Türkiye. Search by manufacturer part number to see stock, prices and datasheets for integrated circuits, microcontrollers, transistors, passive components and connectors, or request a quote for parts that are not listed.

Bu sayfa Entegreci tarafından eklenmiştir. Belgenin önceki sayfaları asıl yayıncısına aittir ve içeriği değiştirilmemiştir; en güncel sürüm için belgenin yayıncısına başvurun.