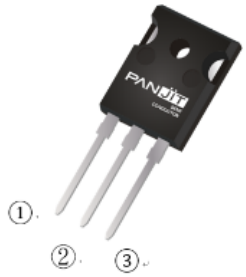
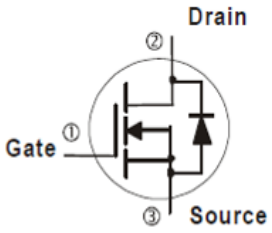
600V N-Channel Super Junction MOSFET

Voltage	**600 V**	**Rdson**	**180 mΩ**
Current	**20.6 A**	**Qg**	**40 nC**

TO-247AD-3LD





Feature:

- $R_{DS(ON)\ Max}$, V_{GS}@10V: 180mΩ
- High Speed Switching and Low $R_{DS(ON)}$
- 100% Avalanche Tested
- 100% Rg Tested
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: TO-247AD-3LD package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 6.231 grams

Application

- TV Power / Industrial Power / PC ATX Power..

Absolute Maximum Ratings (T_A = 25 °C unless otherwise specified)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage @ T_{jmax}		V_{DS}	650	V
Drain-Source Voltage		V_{DS}	600	
Gate-Source Voltage		V_{GS}	±30	
Continuous Drain Current	T_C=25°C	I_D	20.6	A
	T_C=100°C		13	
Pulsed Drain Current (Note 1)	T_C=25°C	I_{DM}	62	A
Single Pulse Avalanche Energy		E_{AS}	420	mJ
MOSFET dv/dt ruggedness		dv/dt	50	V/ns
Power Dissipation	T_C=25°C	P_D	160	W
	T_C=100°C		64	
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C

Thermal Characteristics

PARAMETER		SYMBOL	MAXIMUM	UNITS
Thermal Resistance	Junction-to-Case	$R_{\theta JC}$	0.71	°C/W
	Junction-to-Ambient	$R_{\theta JA}$	50	°C/W

Electrical Characteristics (T_A = 25 °C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250uA	600	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	2.8	-	3.8	
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=10V, I_D=9.5A(Note 1)	-	155	180	mΩ
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=600V, V_{GS}=0V	-	-	1	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±30V, V_{DS}=0V	-	-	±100	nA
Transfer characteristics	gfs	V_{DS}=20V, I_D=20A	-	18	-	S
Dynamic (Note 3)						
Total Gate Charge	Q_g	V_{DS}=480V, I_D=20A, V_{GS}=10V	-	40	-	nC
Gate-Source Charge	Q_{gs}		-	9	-	
Gate-Drain Charge	Q_{gd}		-	17	-	
Input Capacitance	Ciss	V_{DS}=400V, V_{GS}=0V, f=250kHz	-	1410	-	pF
Output Capacitance	Coss		-	50	-	
Reverse Transfer Capacitance	Crss		-	13	-	
Effective Output Capacitance Energy Related (Note 3)	$C_{o(er)}$	V_{DS}=0V to 480V, V_{GS}=0V, f=250kHz (Note 3)	-	68	-	
Turn-On Delay Time	$td_{(on)}$	V_{DD}=300V, I_D=20A, V_{GS}=10V, R_G=25Ω (Note 3)	-	51	-	ns
Turn-On Rise Time	t_r		-	81	-	
Turn-Off Delay Time	$td_{(off)}$		-	174	-	
Turn-Off Fall Time	t_f		-	78	-	
Gate Resistance	Rg	f=1.0MHz	-	8	-	Ω
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S		-	-	20.6	A
Diode Forward Voltage	V_{SD}	I_S=20A, V_{GS}=0V	-	-	1.4	V
Reverse Recovery Charge	Q_{rr}	I_S=20A di/dt=100A/μs	-	6.5	-	μC
Reverse Recovery Time	T_{rr}		-	380	-	ns

NOTES :

1. Pulse width≤300us, Duty cycle≤2%
2. $C_{o(er)}$ is a capacitance that gives the same stored energy as Coss while V_{DS} is rising from 0V to 80% $V_{(BR)DSS}$
3. Guaranteed by design, not subject to production testing

TYPICAL CHARACTERISTIC CURVES

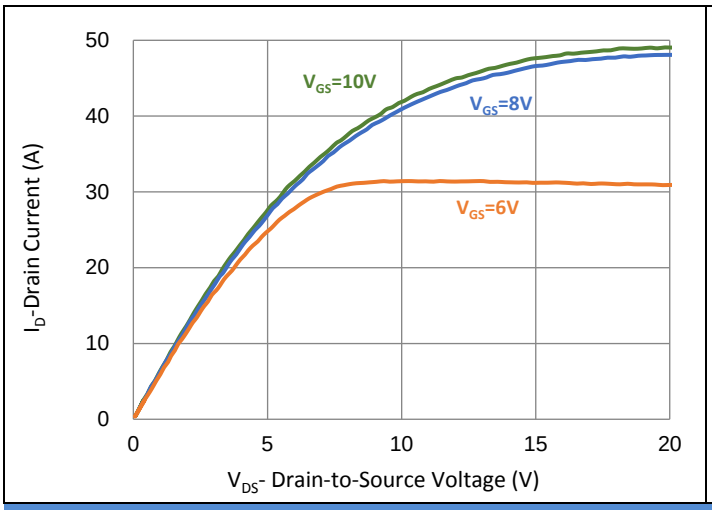


Fig.1 Output Characteristics

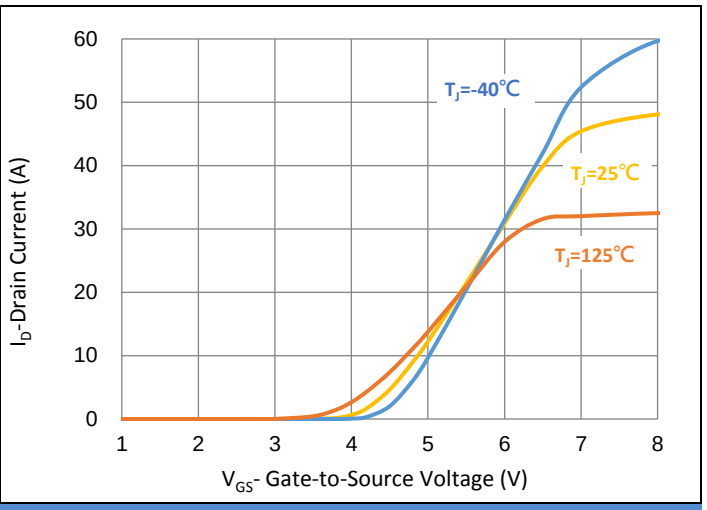


Fig.2 Transfer Characteristics

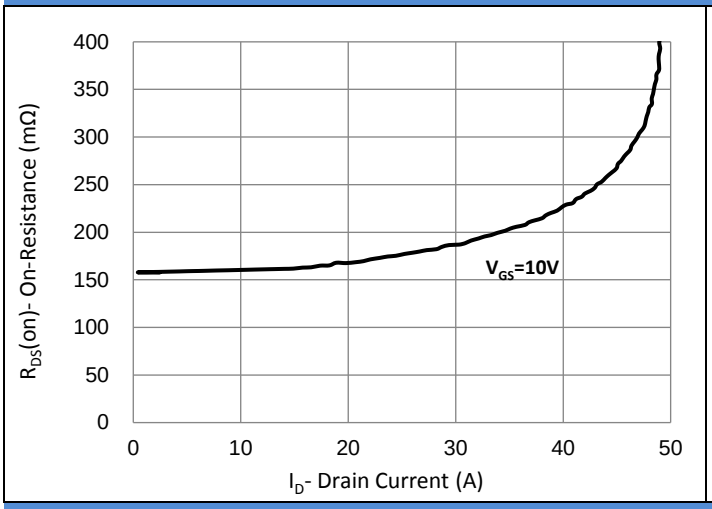


Fig.3 On-Resistance vs. Drain Current

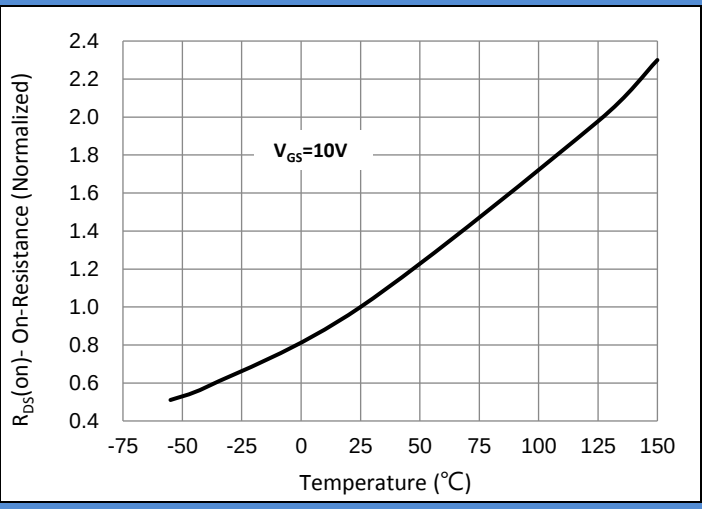


Fig.4 On-Resistance vs. Junction Temperature

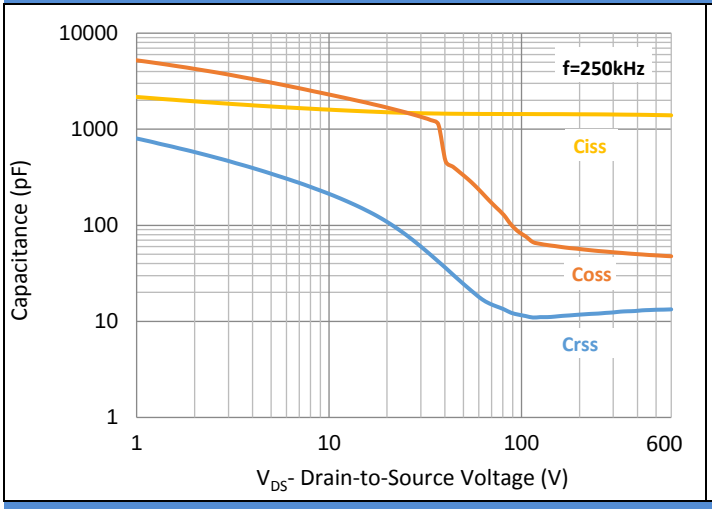


Fig.5 Capacitance vs. Drain-Source Voltage

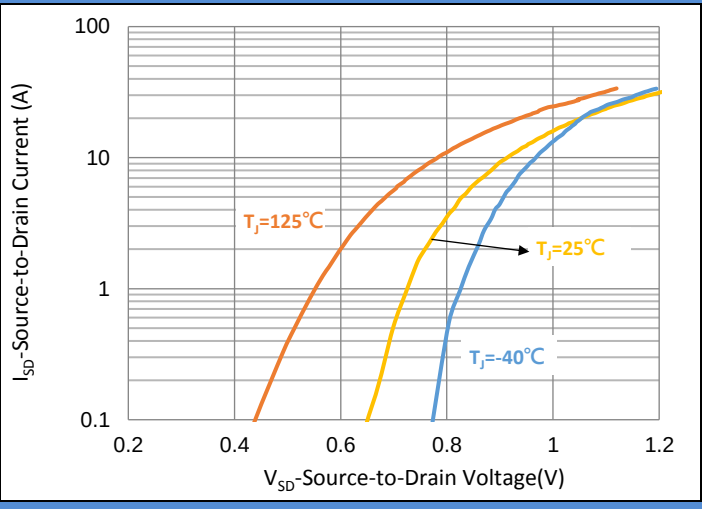


Fig.6 Source-Drain Diode Forward Voltage

TYPICAL CHARACTERISTIC CURVES

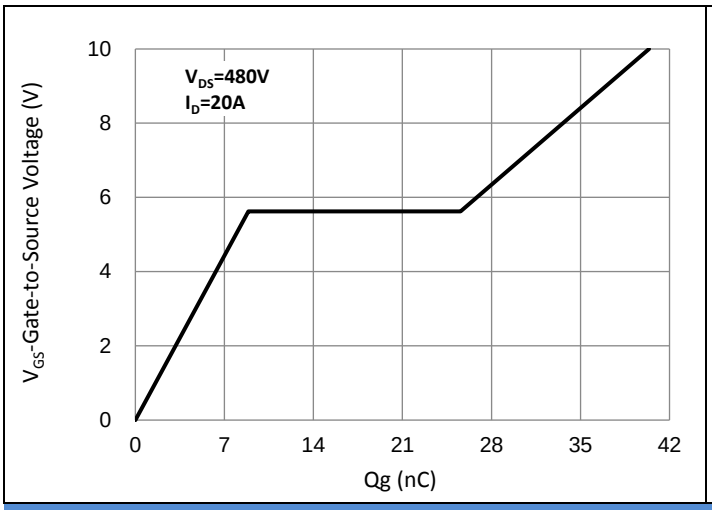


Fig.7 Gate-Charge Characteristics

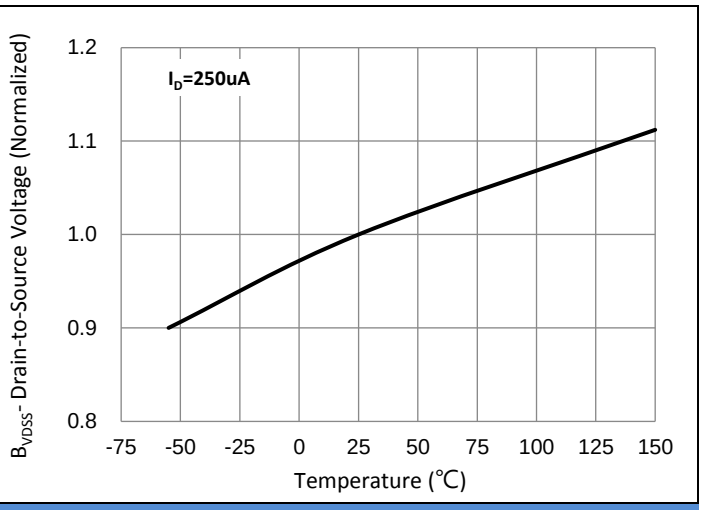


Fig.8 Breakdown Voltage Variation vs. Temperature

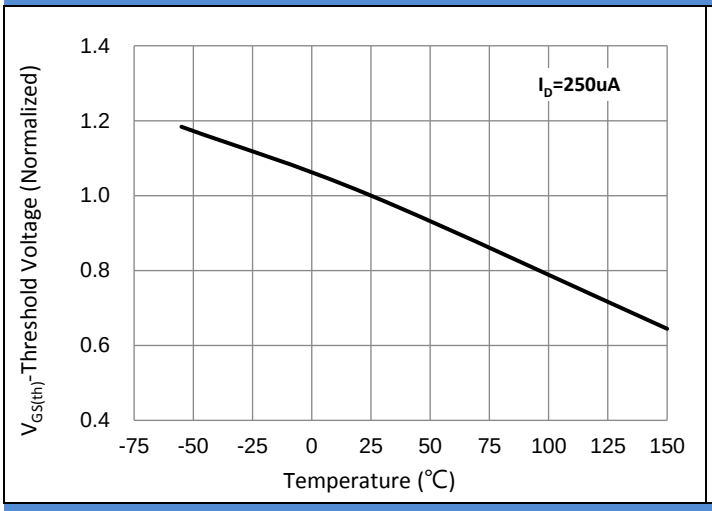


Fig.9 Threshold Voltage Variation with Temperature

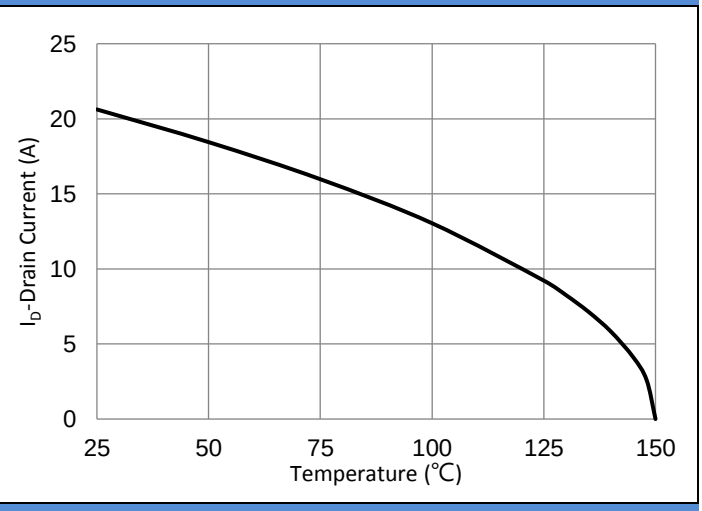


Fig.10 Drain Current vs. Case Temperature

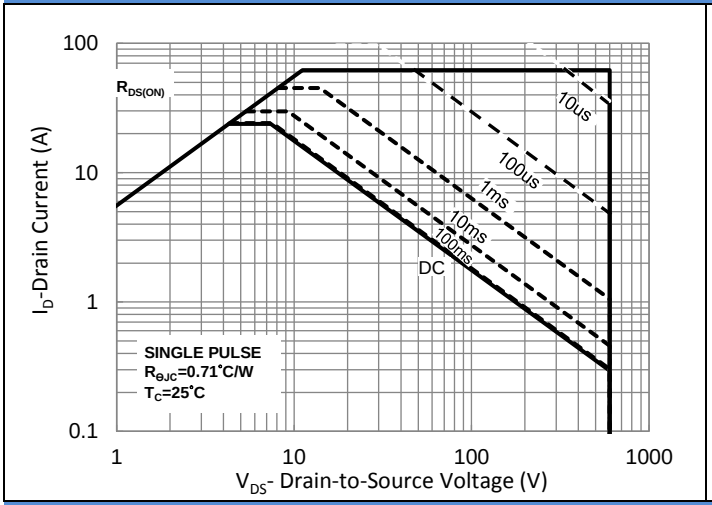


Fig.11 Maximum Safe Operating Area

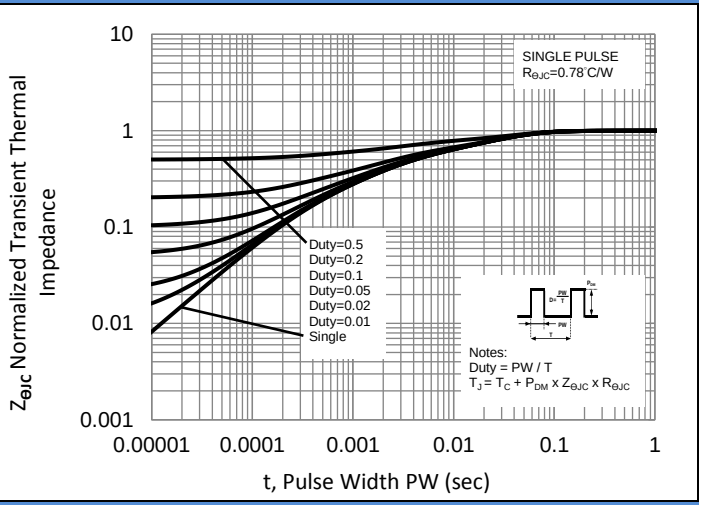


Fig.12 Normalized Transient Thermal Impedance

TYPICAL CHARACTERISTIC CURVES

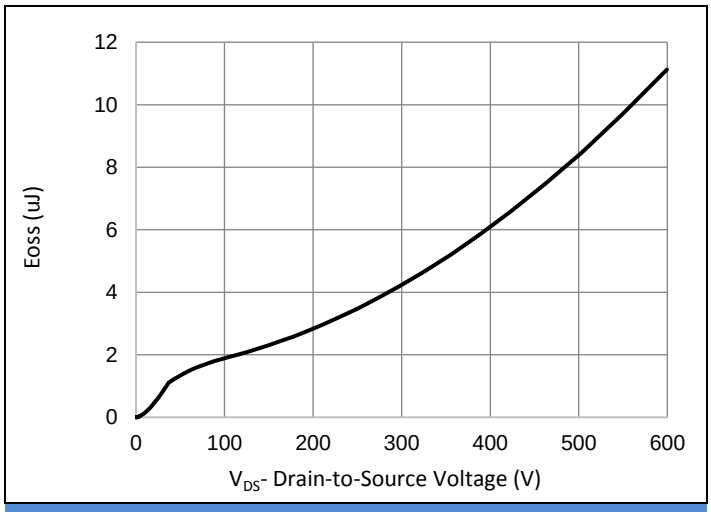


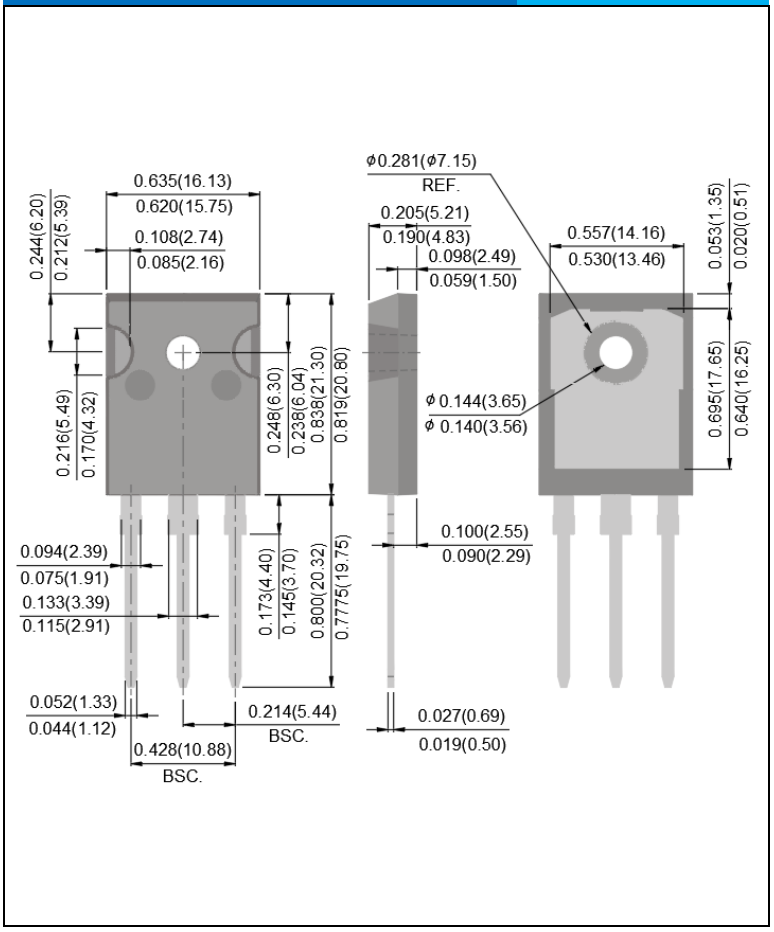
Fig.13 Typ. Coss Stored Energy

Product and Packing Information:

Part No.	Package Type	Packing Type	Marking
PJMH190N60E1	TO-247AD-3LD	30pcs / Tube	190N60E1

Packaging Information

TO-247AD-3LD Dimension — Unit: inch(mm)



Marking Diagram

PJ
190N60E1
YWLL x

Y = Year Code
W = Week Code (A~Z)
LL = Lot Code (00~99)
x = Production Line Code

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