IRLU9343PBF-VB Datasheet

P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)[a]	Q_g (Typ.)
- 60	0.066 at V_{GS} = - 10 V	- 20	40 nC
	0.080 at V_{GS} = - 4.5 V	- 18	

FEATURES

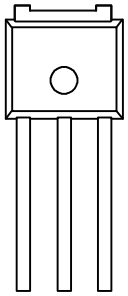
- Trench Power MOSFET
- 100 % UIS Tested

APPLICATIONS

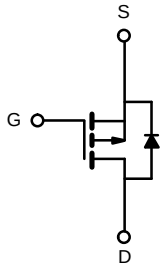
- Load Switch

RoHS COMPLIANT
HALOGEN FREE

TO-251



Top View



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)				
Parameter		**Symbol**	**Limit**	**Unit**
Drain-Source Voltage		V_{DS}	- 60	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	- 20[a]	A
	T_C = 70 °C		- 16	
	T_A = 25 °C		- 11[b]	
	T_A = 70 °C		- 9[b]	
Pulsed Drain Current		I_{DM}	- 100	
Avalanche Current Pulse	L = 0.1 mH	I_{AS}	- 35	
Single Pulse Avalanche Energy		E_{AS}	101	mJ
Continuous Source-Drain Diode Current	T_C = 25 °C	I_S	- 29[a]	A
	T_A = 25 °C		- 2.1[b]	
Maximum Power Dissipation	T_C = 25 °C	P_D	35[a]	W
	T_C = 70 °C		20[a]	
	T_A = 25 °C		3.0[b]	
	T_A = 70 °C		2[b]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		**Symbol**	**Typical**	**Maximum**	**Unit**
Maximum Junction-to-Ambient[b]	Steady State	R_{thJA}	33	40	°C/W
Maximum Junction-to-Case	Steady State	R_{thJC}	0.98	1.2	

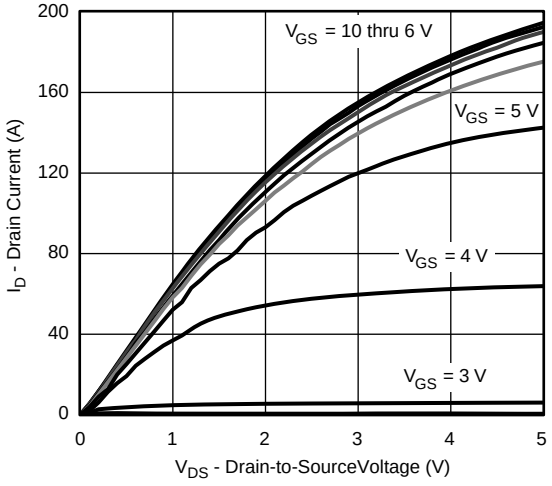
Notes:
a. Based on T_C = 25 °C.
b. Surface mounted on 1" x 1" FR4 board.

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = - 250 µA	- 60			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = - 250 µA		68		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 5.2		
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = - 250 µA	- 1.0		- 2.5	V
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = - 60 V, V_{GS} = 0 V			- 1	µA
		V_{DS} = - 60 V, V_{GS} = 0 V, T_J = 55 °C			- 10	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} = - 5 V, V_{GS} = - 10 V	- 120			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = - 10 V, I_D = - 30 A		0.066		Ω
		V_{GS} = - 4.5 V, I_D = - 20 A		0.080		
Forward Transconductance[a]	g_{fs}	V_{DS} = - 15 V, I_D = - 50 A	20			S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS} = - 25 V, V_{GS} = 0 V, f = 1 MHz		1200		pF
Output Capacitance	C_{oss}			200		
Reverse Transfer Capacitance	C_{rss}			150		
Total Gate Charge	Q_g	V_{DS} = - 30 V, V_{GS} = - 10 V, I_D = - 55 A		40		nC
		V_{DS} = - 30 V, V_{GS} = - 4.5 V, I_D = - 55 A		38		
Gate-Source Charge	Q_{gs}			16		
Gate-Drain Charge	Q_{gd}			19		
Gate Resistance	R_g	f = 1 MHz		5.2		Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = - 2 V, R_L = 2 Ω $I_D \cong$ - 10 A, V_{GEN} = - 10 V, R_g = 1 Ω		10	15	ns
Rise Time	t_r			7	15	
Turn-Off Delay Time	$t_{d(off)}$			70	110	
Fall Time	t_f			40	60	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	T_C = 25 °C			- 66	A
Pulse Diode Forward Current[a]	I_{SM}				- 150	
Body Diode Voltage	V_{SD}	I_S = - 30 A		- 1	- 1.5	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = - 50 A, di/dt = 100 A/µs, T_J = 25 °C		45	68	ns
Body Diode Reverse Recovery Charge	Q_{rr}			59	120	nC
Reverse Recovery Fall Time	t_a			29		ns
Reverse Recovery Rise Time	t_b			16		

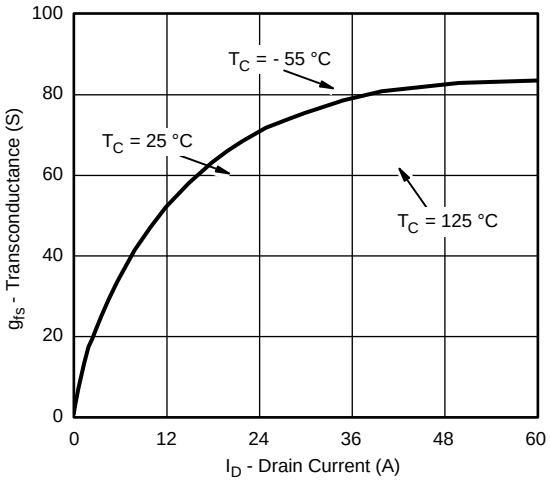
Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

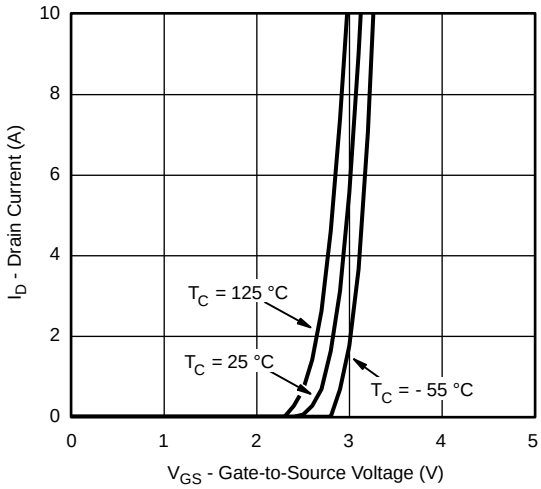
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



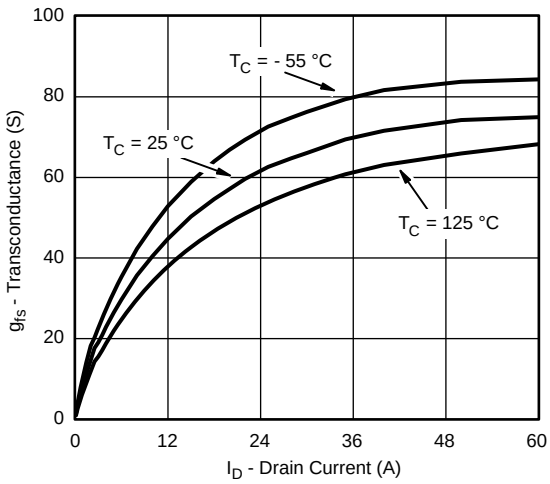
Output Characteristics



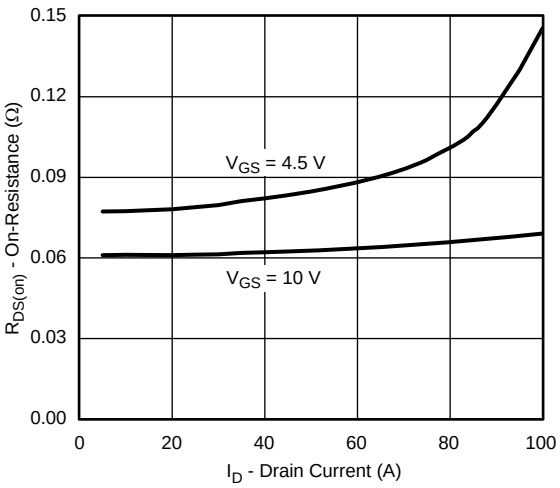
Transconductance



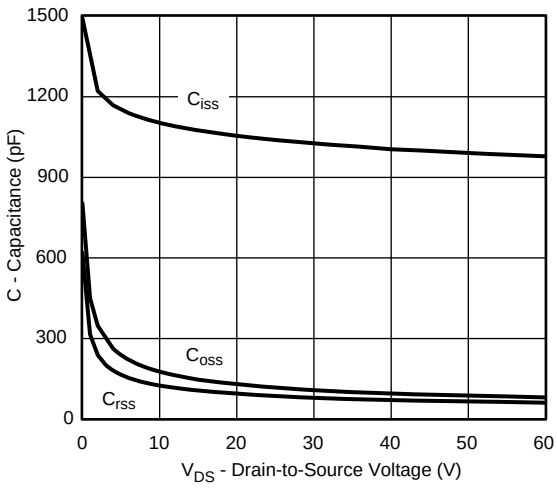
Transfer Characteristics



Transconductance

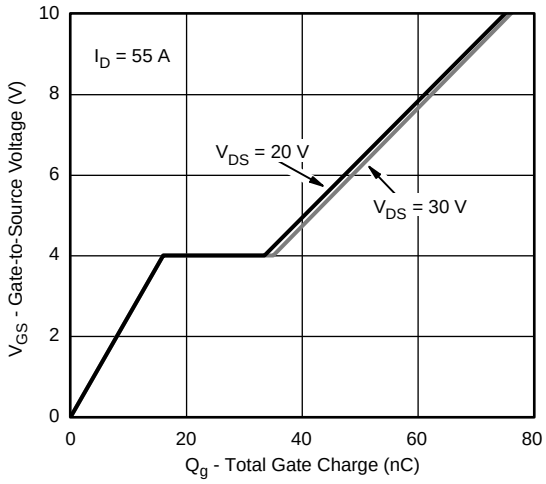


On-Resistance vs. Drain Current

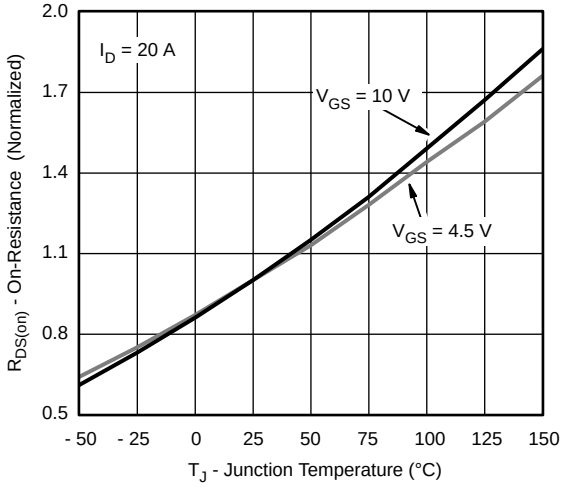


Capacitance

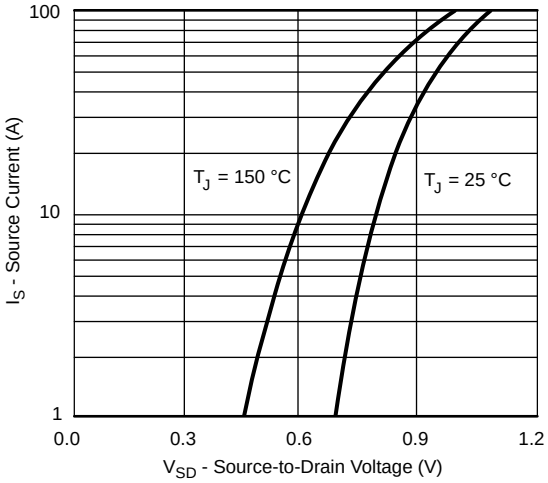
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



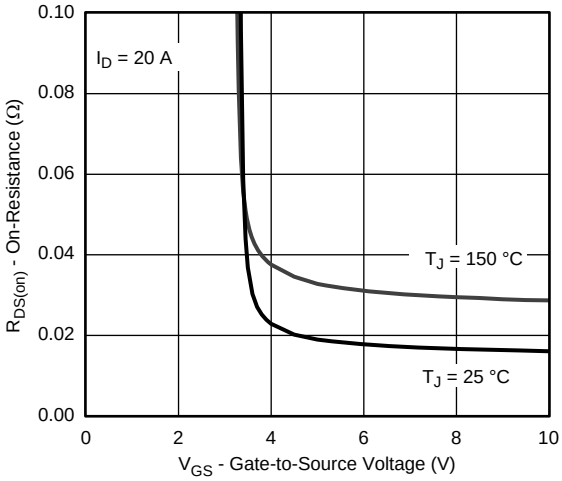
Gate Charge



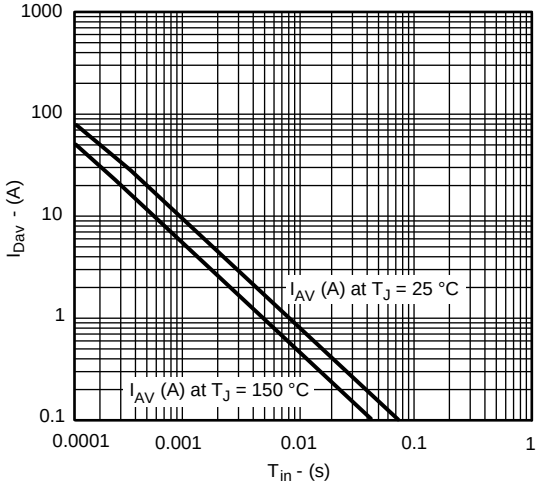
On-Resistance vs. Gate-to-Source Voltage



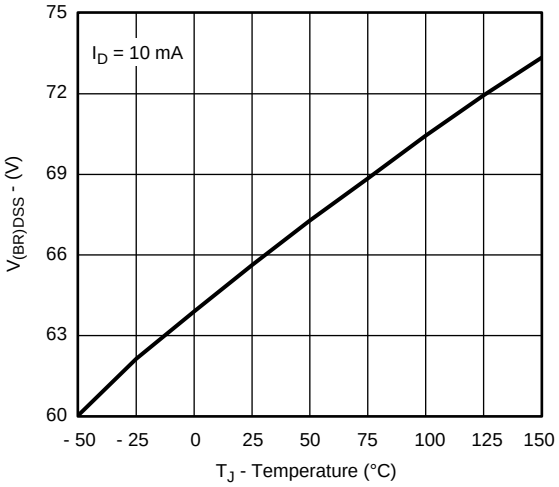
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage

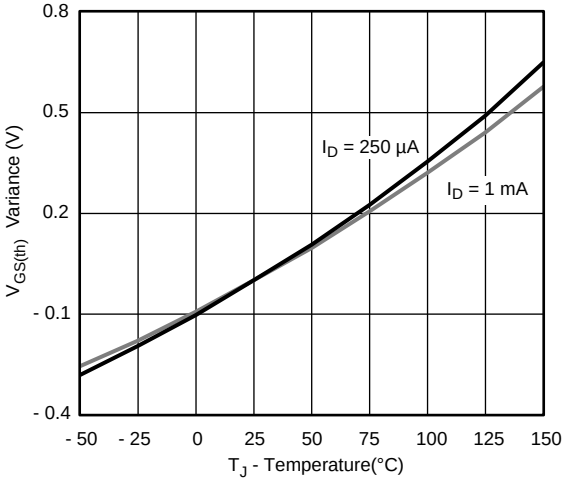


Single Pulse Avalanche Current Capability vs. Time

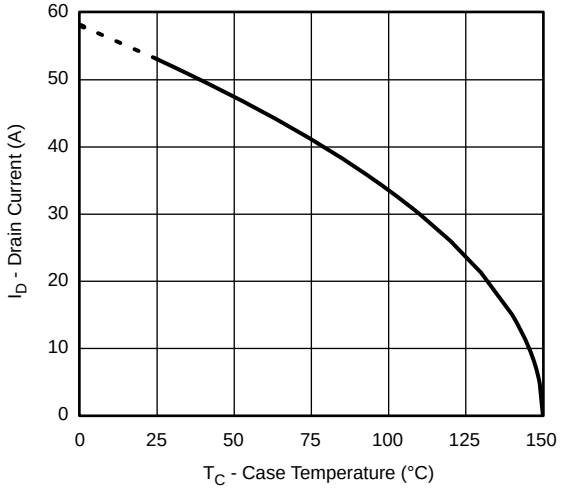


Drain-Source Breakdown Voltage vs. Junction Temperature

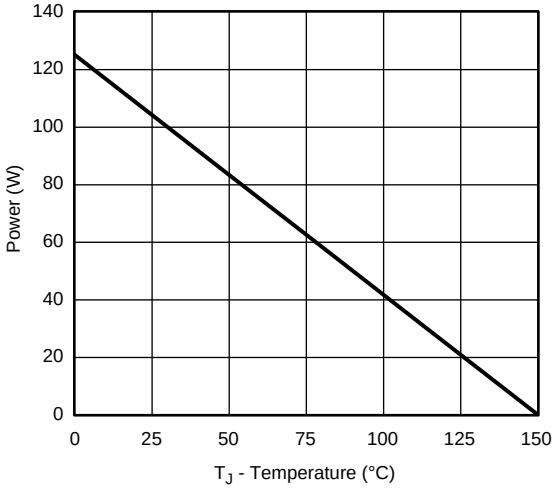
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



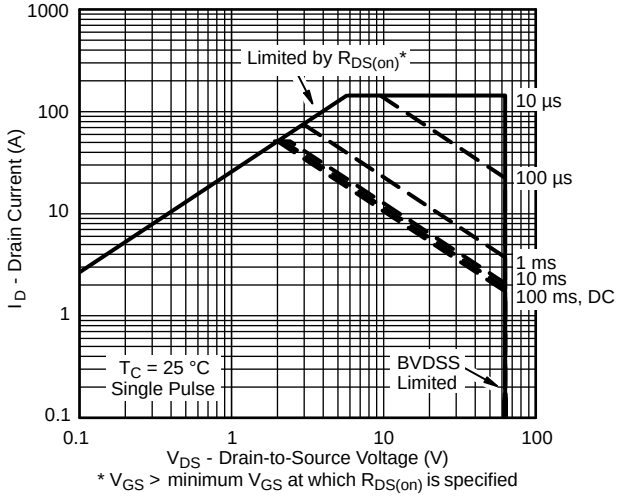
Threshold Voltage



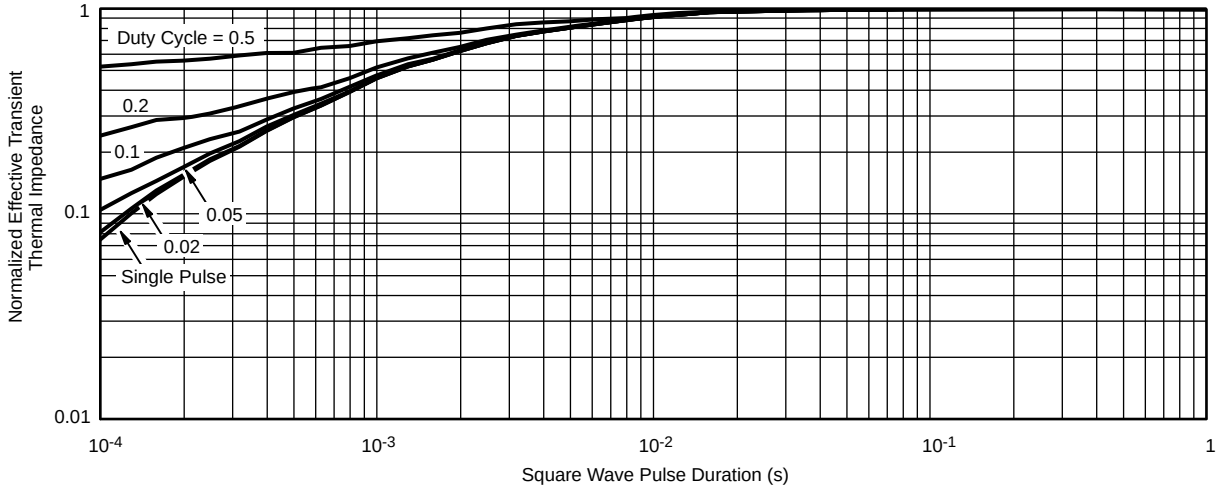
Max. Drain Current vs. Case Temperature



Power Derating, Junction-to-Case

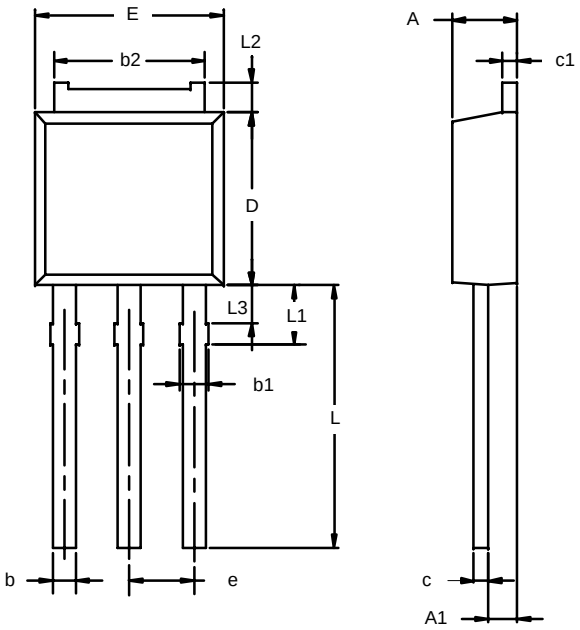


Safe Operating Area, Junction-to-Case



Normalized Thermal Transient Impedance, Junction-to-Case

TO-251AA (DPAK)



Note: Dimension L3 is for reference only.

Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	2.21	2.38	0.087	0.094
A1	0.89	1.14	0.035	0.045
b	0.71	0.89	0.028	0.035
b1	0.76	1.14	0.030	0.045
b2	5.23	5.43	0.206	0.214
c	0.46	0.58	0.018	0.023
c1	0.46	0.58	0.018	0.023
D	5.97	6.22	0.235	0.245
E	6.48	6.73	0.255	0.265
e	2.28 BSC		0.090 BSC	
L	8.89	9.53	0.350	0.375
L1	1.91	2.28	0.075	0.090
L2	0.89	1.27	0.035	0.050
L3	1.15	1.52	0.045	0.060
ECN: S-03946—Rev. E, 09-Jul-01 DWG: 5346				

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