National Semiconductor

LM2679
SIMPLE SWITCHER® 5A Step-Down Voltage Regulator with Adjustable Current Limit

General Description

The LM2679 series of regulators are monolithic integrated circuits which provide all of the active functions for a step-down (buck) switching regulator capable of driving up to 5A loads with excellent line and load regulation characteristics. High efficiency (>90%) is obtained through the use of a low ON-resistance DMOS power switch. The series consists of fixed output voltages of 3.3V, 5V and 12V and an adjustable output version.

The SIMPLE SWITCHER concept provides for a complete design using a minimum number of external components. A high fixed frequency oscillator (260KHz) allows the use of physically smaller sized components. A family of standard inductors for use with the LM2679 are available from several manufacturers to greatly simplify the design process.

Other features include the ability to reduce the input surge current at power-ON by adding a softstart timing capacitor to gradually turn on the regulator. The LM2679 series also has built in thermal shutdown and resistor programmable current limit of the power MOSFET switch to protect the device and load circuitry under fault conditions. The output voltage is guaranteed to a ±2% tolerance. The clock frequency is controlled to within a ±11% tolerance.

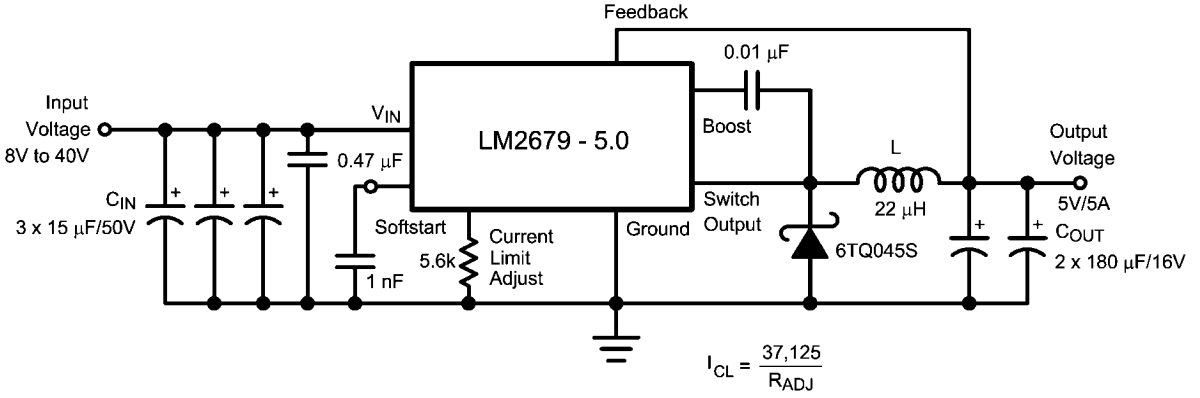
Features

- Efficiency up to 92%
- Simple and easy to design with (using off-the-shelf external components)
- Resistor programmable peak current limit over a range of 3A to 7A.
- 120 mΩ DMOS output switch
- 3.3V, 5V and 12V fixed output and adjustable (1.2V to 37V) versions
- ±2%maximum output tolerance over full line and load conditions
- Wide input voltage range: 8V to 40V
- 260 KHz fixed frequency internal oscillator
- Softstart capability
- −40 to +125°C operating junction temperature range

Applications

- Simple to design, high efficiency (>90%) step-down switching regulators
- Efficient system pre-regulator for linear voltage regulators
- Battery chargers

Typical Application



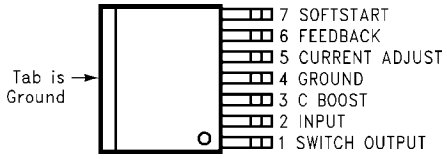
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Connection Diagrams and Ordering Information

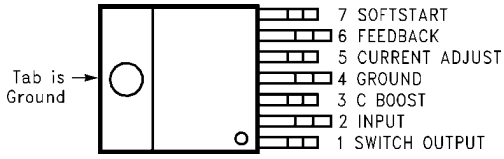
TO-263 Package
Top View



10084701

Order Number
LM2679S-3.3, LM2679S-5.0,
LM2679S-12 or LM2679S-ADJ
See NSC Package Number TS7B

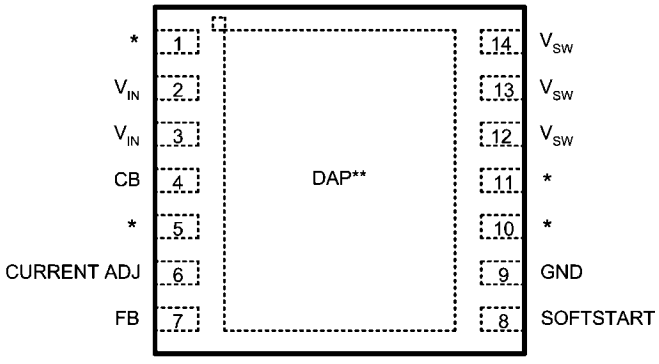
TO-220 Package
Top View



10084702

Order Number
LM2679T-3.3, LM2679T-5.0,
LM2679T-12 or LM2679T-ADJ
See NSC Package Number TA07B

Top View



* No Connections

** Connect to Pin 9 on PCB

10084735

LLP-14
See NS package Number SRC14A

Ordering Information for LLP Package

Output Voltage	Order Information	Package Marking	Supplied As
12	LM2679SD-12	S0003FB	250 Units on Tape and Reel
12	LM2679SDX-12	S0003FB	2500 Units on Tape and Reel
3.3	LM2679SD-3.3	S0003HB	250 Units on Tape and Reel
3.3	LM2679SDX-3.3	S0003HB	2500 Units on Tape and Reel
5.0	LM2679SD-5.0	S0003JB	250 Units on Tape and Reel
5.0	LM2679SDX-5.0	S0003JB	2500 Units on Tape and Reel
ADJ	LM2679SD-ADJ	S0003KB	250 Units on Tape and Reel
ADJ	LM2679SDX-ADJ	S0003KB	2500 Units on Tape and Reel

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Input Supply Voltage	45V
Softstart Pin Voltage	−0.1V to 6V
Switch Voltage to Ground (Note 13)	−1V to V_{IN}
Boost Pin Voltage	V_{SW} + 8V
Feedback Pin Voltage	−0.3V to 14V
Power Dissipation	Internally Limited
ESD (Note 2)	2 kV
Storage Temperature Range	−65°C to 150°C
Soldering Temperature	
Wave	4 sec, 260°C
Infrared	10 sec, 240°C
Vapor Phase	75 sec, 219°C

Operating Ratings

Supply Voltage	8V to 40V
Junction Temperature Range (T_J)	−40°C to 125°C

Electrical Characteristics

Limits appearing in **bold type face** apply over the entire junction temperature range of operation, −40°C to 125°C. Specifications appearing in normal type apply for $T_A = T_J = 25°C$. $R_{ADJ} = 5.6K\Omega$

LM2679-3.3

Symbol	Parameter	Conditions	Typical (Note 3)	Min (Note 4)	Max (Note 4)	Units
V_{OUT}	Output Voltage	V_{IN} = 8V to 40V, 100mA ≤ I_{OUT} ≤ 5A	3.3	3.234/**3.201**	3.366/**3.399**	V
η	Efficiency	V_{IN} = 12V, I_{LOAD} = 5A	82			%

LM2679-5.0

Symbol	Parameter	Conditions	Typical (Note 3)	Min (Note 4)	Max (Note 4)	Units
V_{OUT}	Output Voltage	V_{IN} = 8V to 40V, 100mA ≤ I_{OUT} ≤ 5A	5.0	4.900/**4.850**	5.100/**5.150**	V
η	Efficiency	V_{IN} = 12V, I_{LOAD} = 5A	84			%

LM2679-12

Symbol	Parameter	Conditions	Typical (Note 3)	Min (Note 4)	Max (Note 4)	Units
V_{OUT}	Output Voltage	V_{IN} = 15V to 40V, 100mA ≤ I_{OUT} ≤ 5A	12	11.76/**11.64**	12.24/**12.36**	V
η	Efficiency	V_{IN} = 24V, I_{LOAD} = 5A	92			%

LM2679-ADJ

Symbol	Parameter	Conditions	Typ (Note 3)	Min (Note 4)	Max (Note 4)	Units
V_{FB}	Feedback Voltage	V_{IN} = 8V to 40V, 100mA ≤ I_{OUT} ≤ 5A V_{OUT} Programmed for 5V	1.21	1.186/**1.174**	1.234/**1.246**	V
η	Efficiency	V_{IN} = 12V, I_{LOAD} = 5A	84			%

All Output Voltage Versions Electrical Characteristics

Limits appearing in **bold type face** apply over the entire junction temperature range of operation, −40°C to 125°C.

Specifications appearing in normal type apply for $T_A = T_J = 25°C$. Unless otherwise specified V_{IN}=12V for the 3.3V, 5V and Adjustable versions and V_{IN}=24V for the 12V version.

Symbol	Parameter	Conditions	Typ	Min	Max	Units
DEVICE PARAMETERS						
I_Q	Quiescent Current	$V_{FEEDBACK}$ = 8V For 3.3V, 5.0V, and ADJ Versions $V_{FEEDBACK}$ = 15V For 12V Versions	4.2		6	mA
V_{ADJ}	Current Limit Adjust Voltage		1.21	1.181/**1.169**	1.229/**1.246**	V
I_{CL}	Current Limit	R_{ADJ} = 5.6KΩ, (Note 5)	6.3	5.5/**5.3**	7.6/**8.1**	A
I_L	Output Leakage Current	V_{IN} = 40V, Softstart Pin = 0V V_{SWITCH} = 0V V_{SWITCH} = −1V	 1.0 6		 1.5 15	 mA mA
$R_{DS(ON)}$	Switch On-Resistance	I_{SWITCH} = 5A	0.12		0.14/**0.225**	Ω
f_O	Oscillator Frequency	Measured at Switch Pin	260	**225**	**280**	kHz
D	Duty Cycle	Maximum Duty Cycle Minimum Duty Cycle	91 0			% %
I_{BIAS}	Feedback Bias Current	$V_{FEEDBACK}$ = 1.3V ADJ Version Only	85			nA
V_{SFST}	Softstart Threshold Voltage		0.63	**0.53**	**0.74**	V
I_{SFST}	Softstart Pin Current	Softstart Pin = 0V	3.7		**6.9**	µA
θ_{JA}	Thermal Resistance	T Package, Junction to Ambient (Note 6)	65			°C/W
θ_{JA}		T Package, Junction to Ambient (Note 7)	45			
θ_{JC}		T Package, Junction to Case	2			
θ_{JA}		S Package, Junction to Ambient (Note 8)	56			
θ_{JA}		S Package, Junction to Ambient (Note 9)	35			
θ_{JA}		S Package, Junction to Ambient (Note 10)	26			
θ_{JC}		S Package, Junction to Case	2			++
θ_{JA}		SD Package, Junction to Ambient (Note 11)	55			°C/W
θ_{JA}		SD Package, Junction to Ambient (Note 12)	29			

Note 1: Absolute Maximum Ratings are limits beyond which damage to the device may occur. Operating Ratings indicate conditions under which of the device is guaranteed. Operating Ratings do not imply guaranteed performance limits. For guaranteed performance limits and associated test condition, see the electrical Characteristics tables.

Note 2: ESD was applied using the human-body model, a 100pF capacitor discharged through a 1.5 kΩ resistor into each pin.

Note 3: Typical values are determined with $T_A = T_J = 25°C$ and represent the most likely norm.

Note 4: All limits are guaranteed at room temperature (standard type face) and at **temperature extremes (bold type face)**. All room temperature limits are 100% tested during production with $T_A = T_J = 25°C$. All limits at temperature extremes are guaranteed via correlation using standard standard Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level (AOQL).

Note 5: The peak switch current limit is determined by the following relationship: I_{CL}=37,125/ R_{ADJ}.

Note 6: Junction to ambient thermal resistance (no external heat sink) for the 7 lead TO-220 package mounted vertically, with ½ inch leads in a socket, or on a PC board with minimum copper area.

Note 7: Junction to ambient thermal resistance (no external heat sink) for the 7 lead TO-220 package mounted vertically, with ½ inch leads soldered to a PC board containing approximately 4 square inches of (1 oz.) copper area surrounding the leads.

Note 8: Junction to ambient thermal resistance for the 7 lead TO-263 mounted horizontally against a PC board area of 0.136 square inches (the same size as the TO-263 package) of 1 oz. (0.0014 in. thick) copper.

Note 9: Junction to ambient thermal resistance for the 7 lead TO-263 mounted horizontally against a PC board area of 0.4896 square inches (3.6 times the area of the TO-263 package) of 1 oz. (0.0014 in. thick) copper.

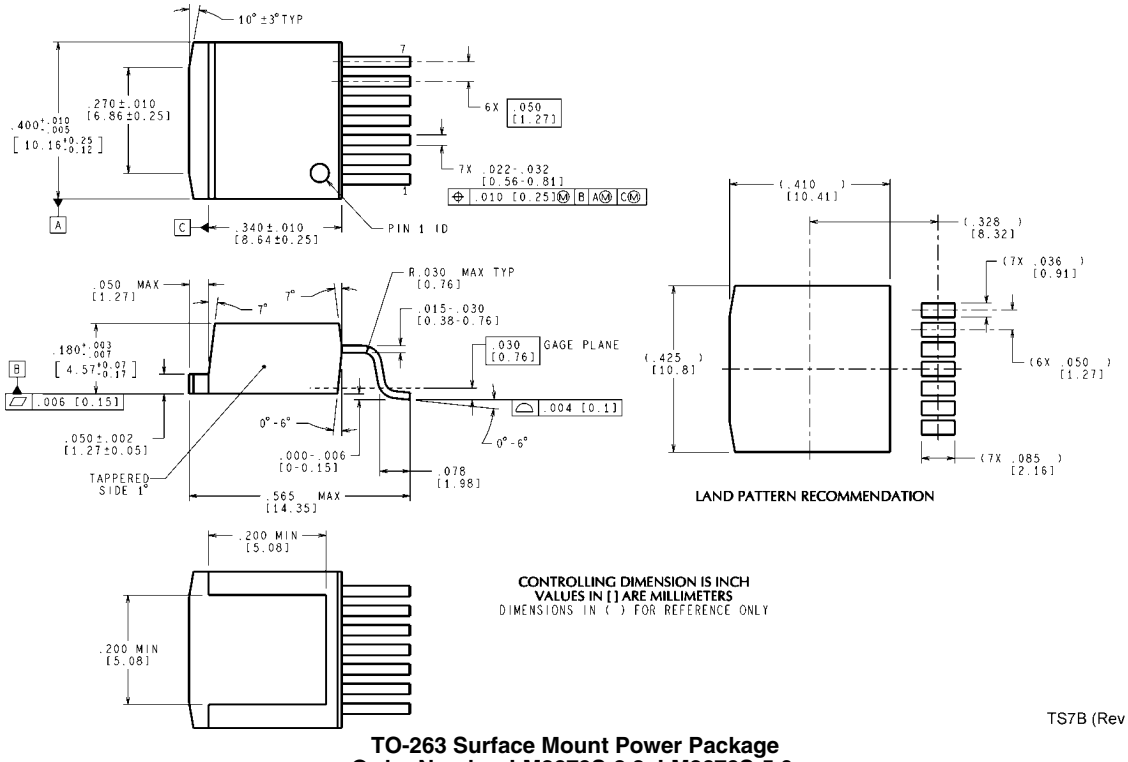
Note 10: Junction to ambient thermal resistance for the 7 lead TO-263 mounted horizontally against a PC board copper area of 1.0064 square inches (7.4 times the area of the TO-263 package) of 1 oz. (0.0014 in. thick) copper. Additional copper area will reduce thermal resistance further. See the thermal model in Switchers Made Simple® software.

Note 11: Junction to ambient thermal resistance for the 14-lead LLP mounted on a PC board copper area equal to the die attach paddle.

Note 12: Junction to ambient thermal resistance for the 14-lead LLP mounted on a PC board copper area using 12 vias to a second layer of copper equal to die attach paddle. Additional copper area will reduce thermal resistance further. For layout recommendations, refer to Application Note AN-1187.

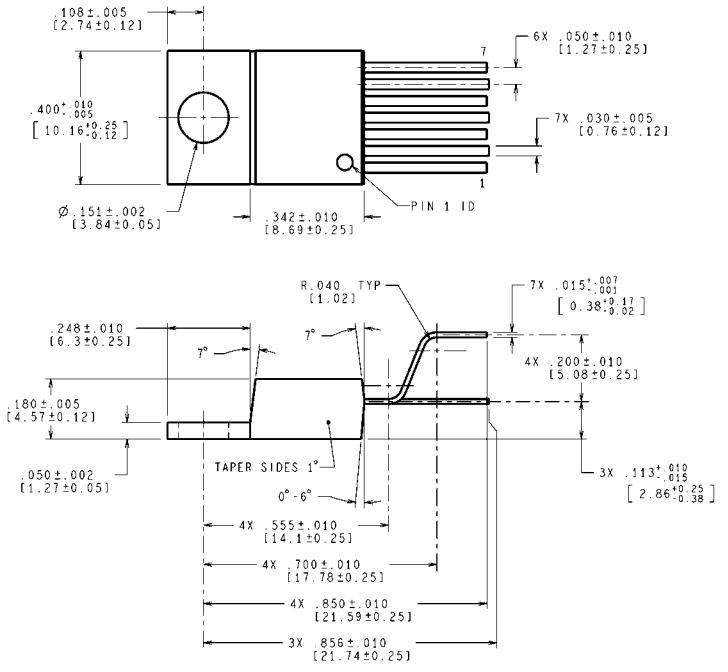
Note 13: The absolute maximum specification of the 'Switch Voltage to Ground' applies to DC voltage. An extended negative voltage limit of -10V applies to a pulse of up to 20 ns, -6V of 60 ns and -3V of up to 100 ns.

Physical Dimensions inches (millimeters) unless otherwise noted



TS7B (Rev E)

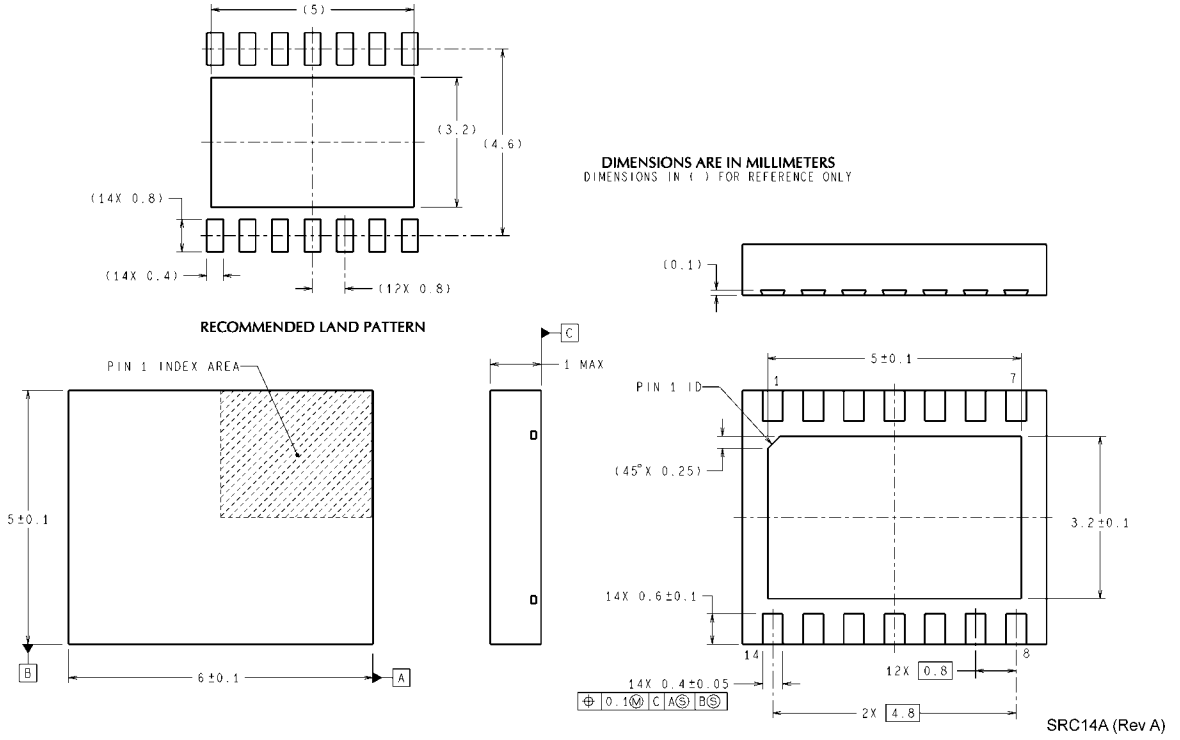
TO-263 Surface Mount Power Package
Order Number LM2679S-3.3, LM2679S-5.0,
LM2679S-12 or LM2679S-ADJ
NS Package Number TS7B



CONTROLING DIMENSIONS IS INCH
VALUES IN [ ] ARE MILLIMETERS

TA07B (Rev E)

TO-220 Power Package
Order Number LM2679T-3.3, LM2679T-5.0,
LM2679T-12 or LM2679T-ADJ
NS Package Number TA07B



SRC14A (Rev A)

14-Lead LLP Package
NS Package Number SRC14A

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